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Application : 10/757	77/Examiner: 7	trA_	GAU:	2816
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**REV 10/04** 

To: 703 746 6830 Page 1 of 3

From: 860 693 1686

#### **CERTIFICATE OF TRANSMISSION**

I HEREBY CERTIFY THAT THIS CORRESPONDENCE IS BEING FACSIMILE TRANSMITTED TO THE PATENT AND TRADEMARKS OFFICE, FAX NO 703 746 6830 ON

10/22/2005 (DATE)

Edward Herbet

**Edward Herbert** 

One Dyer Cemetery Road Canton, CT 06019-2029 eherbert@eherbert.com

Saturday, 22 October, 2005

Rori Burch USPTO Publishing Division Commissioner for Patents P. O. Box 1450 Alexandria, VA 22313-1450 By fax.

Re: 10/707,774, filed January 12, 2004
"Gate Drive Method and Apparatus for Reducing Losses in the Switching of MOSFETs"

Dear Rori Burch:

Following please find the copy of your notice and a signed declaration.

Sincerely yours,

**Edward Herbert** 



#### UNITED STATES PATENT AND TRADEMARK OFFICE

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Serial Number 10707774

Date Mailed 10/12/05

### NOTICE TO FILE CORRECTED APPLICATION PAPERS

#### Notice of Allowance Mailed

This application has been accorded an Allowance Date and is being prepared for issuance. The application, however, is incomplete for the reasons below.

Applicant is given 30 days from the mail date of this Notice within which to correct the informalities indicated below. A failure to reply will result in the application being ABANDONED. This period for reply is NOT extendable under 37 CFR 1.136 (a) or (b).

• Signed oath is missing from application.

APPLICANT MUST SUPPLY MISSING INFORMATION WITHIN 30 DAYS OF THE MAIL DATE OF THIS NOTICE.

A copy of this notice <u>MUST</u> be returned with the reply. Please address response to Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Ron Burch USPTO

Publishing Division Fax (703) 746-6830 Fax (703) 308-6642

## DECLARATION (37 CFR 1.63) FOR UTILITY OR DESIGN APPLICATION USING AN APPLICATION DATA SHEET (37 CFR 1.76)

Electronic Version v11

Stylesheet Version v10

10/707,774

filed 01/12/2004

Title of Invention

Gate Drive Method and Apparatus for Reducing Losses in the Switching of MOSFETs

As the below named inventor, I declare that:

This declaration is directed to the invention titled: "Gate Drive Method and Apparatus for Reducing Losses in the Switching of MOSFETs"

I believe that I am the original and first inventor of the subject matter which is claimed and for which a patent is sought;

I have reviewed and understand the contents of the above-identified application, including the claims, as amended by any amendment specifically referred to above;

I acknowledge the duty to disclose to the United States Patent and Trademark Office all information known to me to be material to patentability as defined in 37 CFR 1.56, including for continuation-in-part applications, material information which became available between the filing date of the prior application and the national or PCT International filing date of the continuation-in-part application.

All statements made herein of my knowledge are true, all statements made herein on information and belief are believed to be true, and further that these statements were made with the knowledge that willful false statements and the like are punishable by fine or imprisonment, or both, under 18 U.S.C. 1001, and may jeopardize the validity of the application or any patent issuing thereon.

**FULL NAME OF INVENTOR:** 

10/22/2005

Inventor: Edward Herbert

Signature: /eh/ Z. June Herbert Citizen of: US